



## U.S. PATENT APPLICATION

SERIAL NUMBER	FILING DATE	CLASS	GROUP ART UNIT
08/707,694	09/04/96	395	2317

APPLICANT

KRISHNA RANGASAYEE, SUNNYVALE, CA; JOHN SHANNON, MORGAN HILL, CA.

DMB none

\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\*\*\*\*\*
VERIFIED

DmB none

FOREIGN FILING LICENSE GRANTED 10/20/96

				FILING FEE RECEIVED	ATTORNEY DOCKET NO.
CA	3	20	3	\$750.00	64.663-063

BARNES KISSELLE RAISCH CHOATE WHITTEMORE

& HULBERT

3500 PENOBSCOT BLDG

645 GRISWOLD ST

**DETROIT MI 48226-4217** 

PROGRAMMABLE LOGIC DEVICE HAVING AN INTEGRATED PHASE LOCK LOOP

TLE

This is to certify that annexed hereto is a true copy from the records of the United States Patent and Trademark Office of the application which is identified above.

By authority of the COMMISSIONER OF PATENTS AND TRADEMARKS

Date

Certifying Officer